

4.13 CP476

4.13.1 General Information

With the addition of the CP476, the B&R SYSTEMS 2003 now has a CPU with outstanding technical properties.

Features

- 750 KByte User SRAM
- 1.5 MByte User FlashPROM
- Additional I/O processor
- System bus for expansions
- CP Interface with four slots
- Two node number switches

Memory capacity was increased to meet the requirements of our customers.

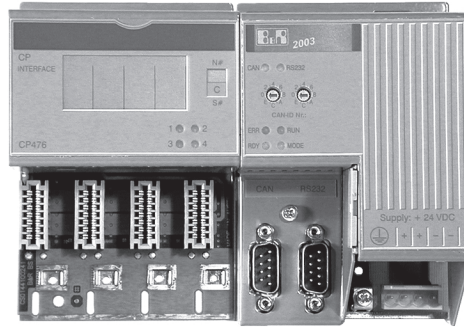
The CP476 extends the CPU performance spectrum for the B&R SYSTEM 2003 in the upward direction. Compared to the CP474, the new CPU increases performance by at least a 50 % because the clock frequency is 50 % higher and the integration of an additional I/O processor. This reduces the interrupt load considerably and decreases the analog update time on the left side by max. the factor 2 (screw-in modules 1 and 2 as well as 3 and 4 are processed parallel to each other). The I/O update on the left side takes place independently of the right side.

The CPU CP476 is equipped with a system bus for expansions. The following expansions can be connected:

- ME010 expansion module for PCMCIA memory cards
- ME020 expansion module for PCMCIA memory cards and a slot for B&R SYSTEM 2005 interface module inserts

Two CAN node number switches guarantee that an offset no longer has to be set. The actual node number always corresponds to the switch setting.

4.13.2 Technical Data



Module ID	CP476
General Information	
Model Number	7CP476.60-1
Short Description	2003 CPU, 750 KB SRAM, 1,5 MB FlashPROM, 24 VDC, 12,5 W supply, 1 RS232, 1 CAN interface, CAN: electrical isolation, network capable, 4 slots for screw-in modules, system bus for expansion modules, max. 272 digital / 80 analog I/O points
C-UL-US Listed	in preparation
Module Type	B&R2003 CPU
Module Width	B&R 2003 double width
Module Slot	1 + 2
Processor Section	
Additional I/O Processor	Handles I/O data points
Instruction Cycle Time (average value for 70 % bit and 30 % analog processing)	0,5µs
Standard Memory User RAM System PROM User PROM	750 KByte SRAM 512 KByte FlashPROM 1.5 MByte FlashPROM
Data Buffering Backup Battery Buffer Current Typical Maximum	Lithium Battery 3 V / 950 mAh 2,2 µA 110 µA
HW Watchdog	Yes
Voltage Monitoring	The internal supply is monitored for overvoltage and undervoltage

Module ID	CP476
Peripheral	
Real-time Clock Resolution	Nonvolatile 1 s
Status Display	LEDs
I/O Bus Interface (right side)	9 pin D-type socket
System Bus for Expansions (left side)	PCMCIA memory card ME010 B&R SYSTEM 2005 interface module insert ME020
Slots for Screw-in Modules Suitable for IF Modules	4 1 - 3
Standard Communication Interfaces	
Application Interface IF1 Electrical Isolation Connection Max. Distance Max. Baudrate	RS232 No 9 pin D-type plug 15 m / 19200 Baud 115.2kBaud
Application Interface IF2 Electrical Isolation Connection Max. Distance Max. Baudrate	CAN Yes 9 pin D-type plug 1000 m 500 kBaud
Power Supply	
Input Voltage Minimum Nominal Maximum	18 VDC 24 VDC 30 VDC
Power Consumption	Max. 20 W
Output Power for I/O Ports	12.5 W ¹⁾

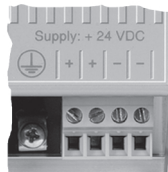
¹⁾ Integrated power supply on pin 4 of the RS232 interface for simple PANELWARE controllers, e.g. P120.

4.13.3 Status Display

LED	Meaning
CAN	Data transfer to or from CAN controller
RS232	Indicates if data is being transmitted or received
ERR	Lit in Service mode
RUN	Lit in RUN and in Service mode
RDY	Lit in Service mode
MODE	Lit when programming FlashPROM
1, 2, 3, 4	These LEDs show the operating state of the respective screw-in module.
Not lit	Screw-in module defective or not inserted
Blinking slowly	Communication error with screw-in module
Blinking quickly	Screw-in module is new or has been exchanged with another module type
Lit	Screw-in module is ready for operation

4.13.4 Power Supply

The CPU CP476 is supplied with 24 VDC. The pin assignments are printed on the module. Both + pins are connected and both – pins are connected internally.



4.13.5 Interfaces

The CPU has two interfaces:



CAN RS232

4.13.6 CAN Bus

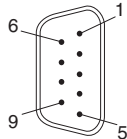
The electrically isolated standard field bus interface is used for the following tasks:

- Communication with other control systems
- Remote I-O expansion using B&R 2003 components and a CAN bus controller

It is recommended that you use the T-connector AC911 (see Chapter 7 "General Accessories") when coupling to a CAN network. A terminal resistance is integrated into the T-connector for the bus termination, which can be switched on or off.

For more information on wiring CAN field bus systems, see chapter 2, "Project Planning and Installation", section "CAN Field Bus".

9 pin D-type plug

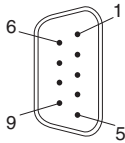


Pin	Assignment
1	n. c.
2	CAN_L
3	CAN_GND
4	n. c.
5	n. c.
6	res.
7	CAN_H
8	n. c.
9	n. c.

4.13.7 RS232 Interface

This non-electrically isolated interface is primarily intended for programming the CPU. The RS232 interface can also be used as a general interface (e.g. P121 visualization, printer, bar code reader, etc.).

9 pin D-type plug



Pin	Assignment	
1	n. c.	Reserved
2	RXD	Receive Signal
3	TXD	Transmit Signal
4	+5 VDC / max. 500 mA	Panel Supply
5	GND	Ground
6	n. c.	Reserved
7	RTS	Request To Send
8	CTS	Clear To Send
9	n. c.	Reserved

4.13.8 CAN Node Number Switch



The CAN node number is set with the two hex switches. The switch setting can be evaluated by the application program at any time. If the switch position is changed during operation, a warning can be generated. The operating system only interprets the switch position when switched on. The settings 00, FD and FF are reserved for special functions.

Switch Position	Description
00	Programming System Flash (see respective section)
FD	Update MEMCARD (see ME010 and ME020)
FF	Diagnose mode

4.13.9 Programming System Flash

General Information

The CPUs are delivered with an operating system. The operating system can be updated using the programming system.

An operating system update is possible using PG2000 V 2.41 and higher.

Automation Studio™ Support: See Automation Studio™ Help starting with V 1.40

Operating System Update

When updating the operating system, the following procedure must be followed:

- 1) Establish online connection between programming device (PC or Industrial PC) and the CPU.
- 2) Start PG2000 programming system.
- 3) In PG2000, call the function *PCCSW Update* (see menu item *Service* in the pull-down menu *System*).
- 4) A dialog box is shown where you can set the transfer rate (baudrate) for the update procedure and the PC interface used for the online connection (e.g. 57600 Baud, COM1).
- 5) Another dialog box is opened when you select [OK].
- 6) The operating system version can be selected in this dialog box. After closing this dialog box by selecting [Yes], the system ROM is deleted (incl. operating system). Then the selected operating system version is transferred to the system ROM. The update progress is shown in the message line.



The User Flash will be deleted!

- 7) Switch PCC off and then on again.
- 8) The PCC is now ready for use.



The operating system update is not only possible via an online connection, it is also possible via a CAN network or a serial network (INA2000 protocol).

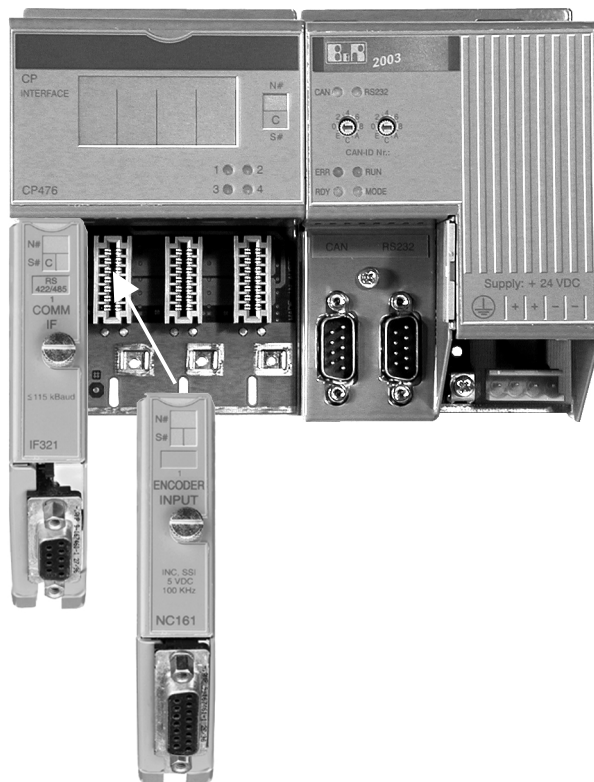
4.13.10 CP Interface

The CPU is equipped with four slots for screw-in modules. The required screw-in modules are inserted into the CP interface and screwed firmly into place.

To reduce the load on the CPU, an additional I/O processor handles operation of the all I/O data points on the CP interface and the I/O bus interface (right side).

The I/O processor handles slots 1 and 3 as well as 2 and 4 in succession (multiplex operation). That means slots 1 and 2 as well as 3 and 4 are operated parallel to each other.

The screw-in interface modules can be used with slots 1, 2 and 3.



Overview

The following screw-in modules can be used on the CP Interface.

Module	Type	Description
7AI261.7	Analog IN	1 input to evaluate a full-bridge strain gauge
7AI294.7	Analog IN	4 inputs for potentiometer displacement gauge
7AI351.70	Analog IN	1 x ± 10 V or 1 x 0 - 20 mA (1 x ± 20 mA also possible) Potentiometer operation
7AI354.70	Analog IN	4 x ± 10 V
7AI774.70	Analog IN	4 x 0 - 20 mA (4 x ± 20 mA also possible)
7AO352.70	Analog OUT	2 x ± 10 V / 0 - 20 mA
7AT324.70	Analog IN	4 x Temperature sensor (PT100, PT1000, KTY10 or KTY84)
7AT352.70	Analog IN	2 x PT100 3-wire
7AT664.70	Analog IN	4 x Thermocouple
7DI135.70	Digital IN	4 x 24 VDC, 50 kHz
7DO135.70	Digital OUT	4 x 12 - 24 VDC, 0.1 A, 100 kHz
7DO164.70	Digital OUT	4 x 48 - 125 VAC, 50 mA, nonvolatile input
7IF311.7	Interface	1 x RS232
7IF321.7	Interface	1 x RS485/RS422
7IF361.70-1	Interface	1 x PROFIBUS DP-Slave
7IF371.70-1	Interface	1 x CAN
7NC161.7	Encoder Module	1 x 100 kHz, 5 / 24 VDC

Commands

The following commands can be used on the CP Interface:

- Reading the screw-in module type
- Switching off automatic mode
- Switching on automatic mode

The commands are described in section "AF101".

4.13.11 Legend Sheets

A legend sheet can be slid into the front of the CPU CP476 from above. These sheets can be used for labelling the screw in modules.

4.13.12 Data/Real-time Clock Buffering

The battery voltage is checked cyclically. The cyclic load test of the battery does not considerably shorten the battery life, instead it gives an early warning of weakened buffer capacity. The status information, "Battery OK" is available from the B&R-TRAP function, "SYS_battery".

4.13.13 System Variable SYS2003

General Information

The system variable SYS2003 is a structure containing the elements "io_scan" and "io_refresh". It must be declared in a task as PCC global.

Element	Variable Type	Description
io_scan	INT16	Duration of the last I/O cycle in μ s
io_refresh	INT8	0 I/O data is older than one cycle 1 I/O data is current



If digital I/O variables are used in the HSTC, the system variable SYS2003 will also be placed in the HSTC. Values in lower task classes will therefore not be consistent. If no digital I/Os are placed in the HSTC, the SYS2003 variable will use the 10 ms operating system clock.

4.13.14 Exchanging the Battery

Battery Data

Lithium Battery	3 V / 950 mAh
Model Number	0AC201.9 (5 lithium batteries)
Storage Time	Max. 3 years at 30 °C
Relative Humidity	0 to 95 % (non-condensing)

Buffer Duration

Buffer Current	CP476
Typical	2.2 μ A
Maximum	110 μ A



B&R recommend changing the batteries after five years of operation.

Procedure

The product design allows battery changing to be carried out with the PCC switched on or off. In some countries, safety regulations do not allow batteries to be changed while the module is switched on.

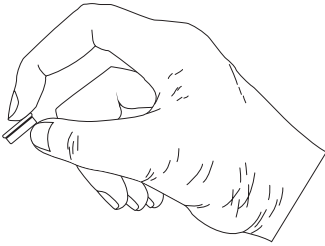


Data stored in RAM will be lost if the battery is changed with the PCC switched off!

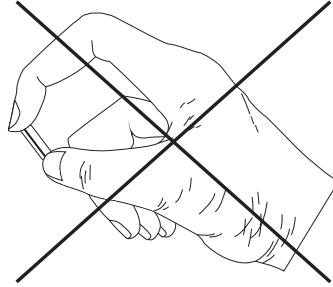
- 1) Touch the mounting rail or earth connection (not the power supply!) in order to discharge any electrostatic charge from your body.
- 2) Remove the cover from the lithium battery holder using a screwdriver.

- 3) Remove the battery from the holder by pulling the removal strip (don't use uninsulated tools -> risk of short circuiting).
The battery should not be held by its edges. **Insulated** tweezers may be used for removing the battery.

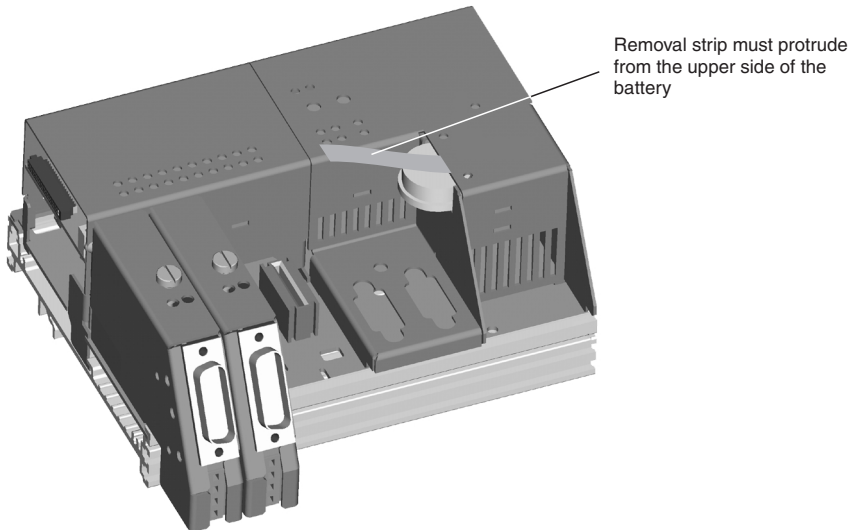
Right:



Wrong:

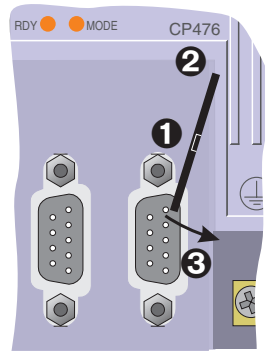


- 4) Insert the new battery with correct polarity. The removal strip should be protruding from the battery holder and the "+" side of the battery should be facing downward.
In order to be able to remove the battery again in future, the removal strip **must** protrude from the **upper side** of the battery.



- 5) Now wrap the end of the removal strip over the top of the battery and insert it underneath the battery so that it does not protrude from the battery holder.

- 6) Replace cover. Ensure that the slot in the edge of the cover faces the front of the module **1**. Insert the upper edge of the cover in the battery holder opening **2**. Press the lower end of the cover home firmly **3**.



Lithium batteries are considered hazardous waste! Used batteries should be disposed of accordingly.